

438	433	
Class	Subclass	
ISSUE CLASSIFICATION		

PATENT NUMBER

DEST AVAILABLE COR

U.S. UTILITY Patent Application

O.J.P.E.

PATENT DATE

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SCANNED

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APPLICATION NO. 09/823274	CONT/PRIOR F	CLASS 438	SUBCLASS 433	ART UNIT 0010	EXAMINER BROCK
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APPLICANTS

Walter Coster
Meindert Lunenburg
Inard Alain
Jos Guelen

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MOS transistor in an integrated circuit and active area forming method

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. <i>4</i>	Figs. Drwg. <i>9</i>	Print Fig. <i>4C, 4D</i>	Total Claims <i>3</i>	Print Claim for O.G. <i>1</i>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	<i>Paul E Brock II</i> <i>1/21/04</i> (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	<i>Tom Thomas</i> Supervisory Patent Examiner Technology Center 2800 _____ (Primary Examiner)			ISSUE FEE	
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.				Amount Due	Date Paid
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